

Notice of Allowability	Application No.	Applicant(s)	
	10/627,796	DOSLUOGLU, TANER	
	Examiner Steven Loke	Art Unit 2811	

-- The MAILING DATE of this communication app ars on the cov r sh et with the corr spond nce addr ss--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to amendment filed on 11/19/04.
2. The allowed claim(s) is/are 1-26.
3. The drawings filed on 19 November 2004 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

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1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Larry J. Prescott on 1/27/05.

The application has been amended as follows:

Claim 16, line 4, change "determining" to --reading--.

Claim 20, line 1, change "determining" to --reading--.

Claim 26, line 3, after "transistor", insert --circuit--.

2. The following is an examiner's statement of reasons for allowance: The first major difference in the claims not found in the prior art of record is a method comprising: resetting the potential between said P well and said substrate during a reset period wherein a tunneling current between said P well and said floating gate resets the potential of said floating gate; accumulating charge at the PN junction between said P type silicon substrate and said N well during a charge integration period, thereby changing the potential of said N well, said P well, and said floating gate and wherein said charge integration period follows said reset period; and reading the potential of said floating gate after said charge integration period has been completed. The second major difference in the claims not found in the prior art of record is a method comprising:

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resetting the potential between said N well and said substrate during a reset period wherein a tunneling current between said N well and said floating gate resets the potential of said floating gate; accumulating charge at the PN junction between said N type silicon substrate and said P well during a charge integration period, thereby changing the potential of said P well, said N well, and said floating gate and wherein said charge integration period follows said reset period; and reading the potential of said floating gate after said charge integration period has been completed. The third major difference in the claims not found in the prior art of record is the means for reading the potential of said floating gate, wherein the only electrical connection to said floating gate is to connect said floating gate to said means for reading the potential of said floating gate. The fourth major difference in the claims not found in the prior art of record is the means for determining the potential of said floating gate, wherein the only electrical connection to said floating gate is to connect said floating gate to said means for determining the potential of said floating gate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (571) 272-1657. The examiner can normally be reached on 7:50 am to 5:20 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

sl

January 27, 2005

Steven Sohe
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